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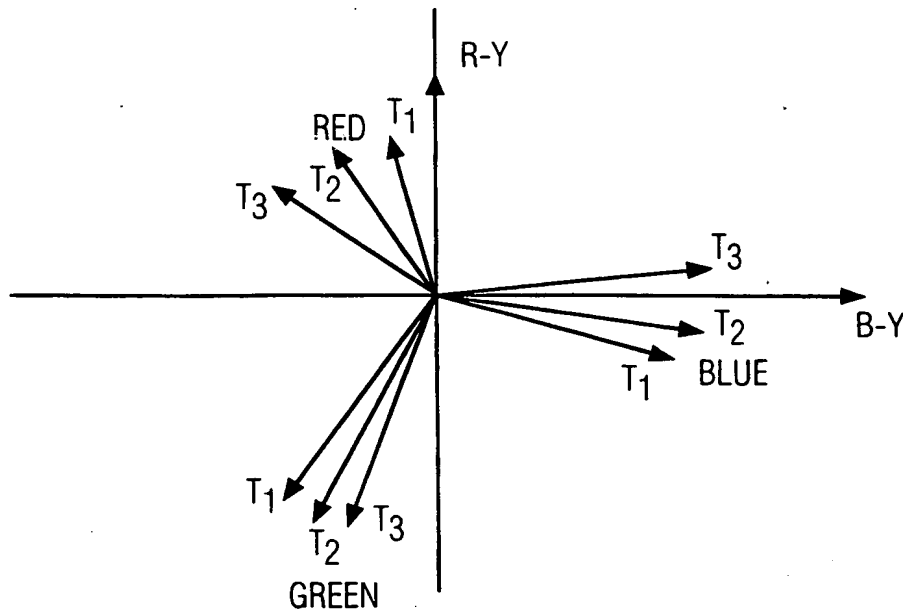


FIG. 1

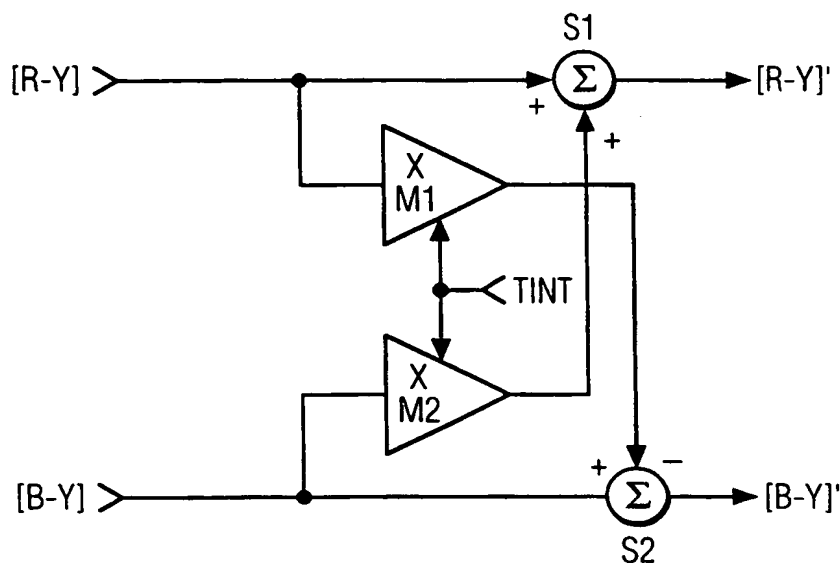


FIG. 2

The diagram shows a circuit with two comparators, A1 and A2, and two multiplexers, M1 and M2. The inputs to the comparators are [R-Y] and [B-Y]. The outputs of the comparators are connected to the inputs of the multiplexers. The multiplexers are controlled by a common reference voltage derived from a divider. The outputs of the multiplexers are connected to the outputs [R-Y]' and [B-Y]'. The circuit is powered by Vcc and ground.

FIG. 4

[illegible]

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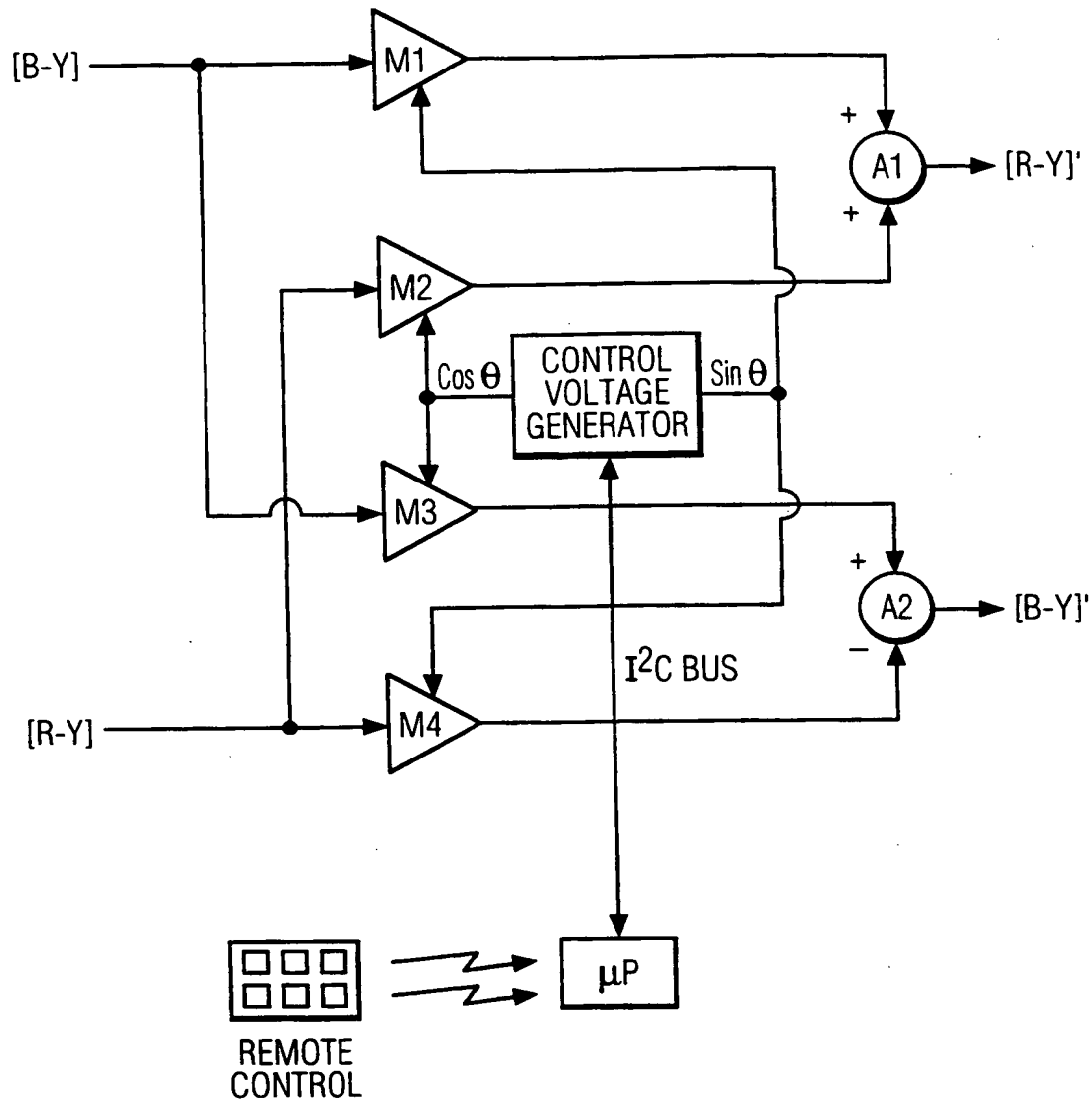


FIG. 5

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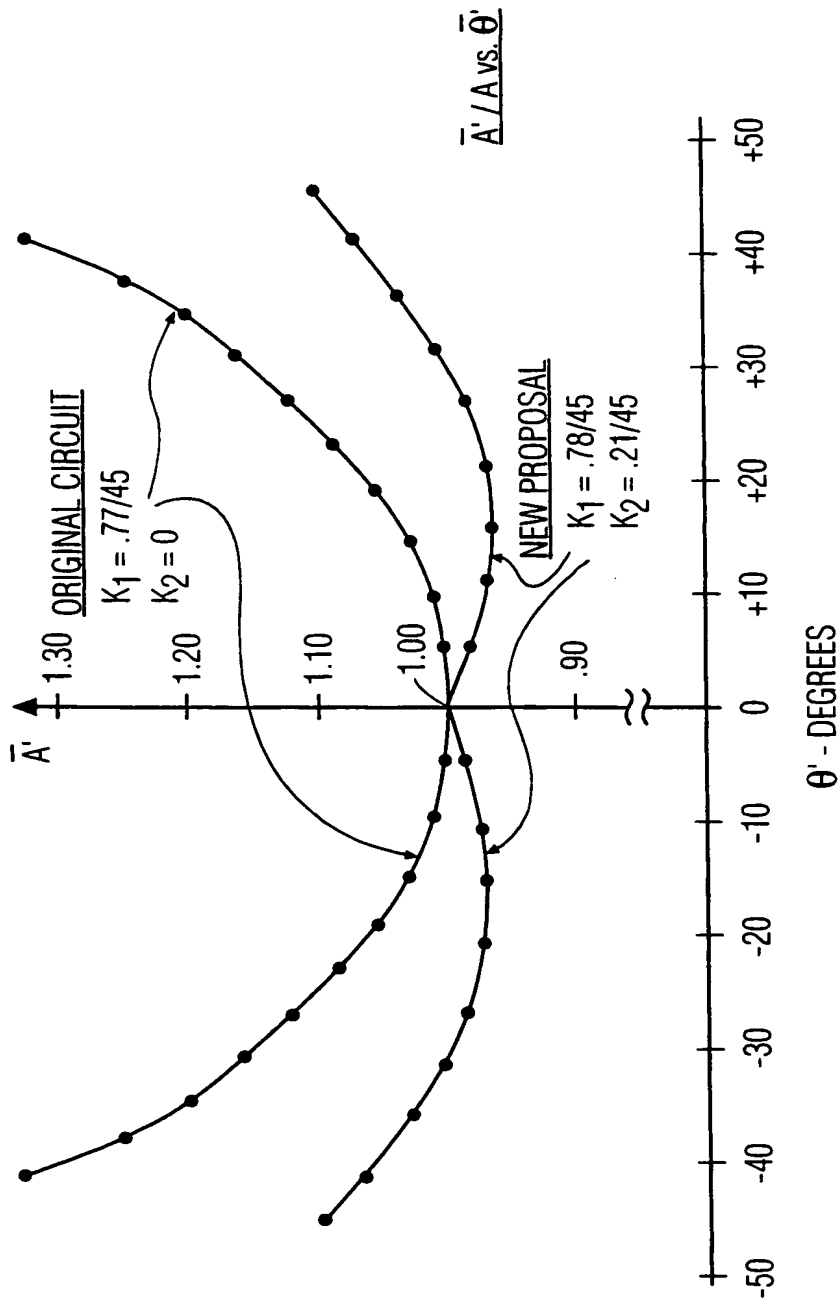


FIG. 6